

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

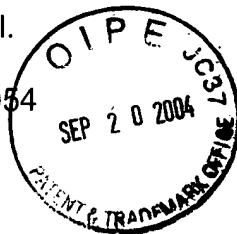
Carl M. Ellison et al.

Application No.: 09/538,954

Docket No.: P8107

Filed: 03/31/00

For: Generating Isolated Bus Cycles For
Isolated Execution



Examiner: Tremayne M. Norris

Art Unit: 2137

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(d)

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find an Information Disclosure Statement, which is being submitted after the mailing of a Final Office Action but before the payment of any issue fee. A copy of each cited reference is also enclosed. Also enclosed is a check in the amount of \$180.00 to cover the fee under 37 C.F.R. 1.17(p).

In accordance with 37 C.F.R. § 1.97(d)-(e), I hereby state that no item of information in the Information Disclosure Statement filed herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than 3 months prior to the filing of this Information Disclosure Statement.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application, and is not to be construed as an admission that the information cited in this statement is material to patentability.

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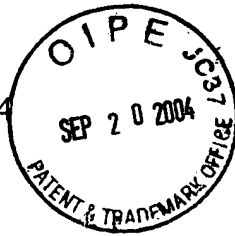
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CROSS REFERENCE TO RELATED APPLICATIONS

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby cross-reference the following related patents and patent applications, which are all assigned to the same assignee as the present patent application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313 on:

9/15/04

Date of Deposit

Gayle Bekish

Name of Person Mailing Correspondence

Gayle

Signature

9/15/04

Date

Reference No.	Application Serial No.	Patent No.	Title
1	09/672,603		Controlling Accesses To Isolated Memory Using A Memory Controller For Isolated Execution
2	09/822,986		Checking File Integrity Using Signature Generated In Isolated Execution
3	09/668,585		Managing A Secure Platform Using A Hierarchical Executive Architecture In Isolated Execution Mode
4	09/668,610		Protecting Software Environment In Isolated Execution
5	09/539,348	6,760,441	Generating A Key Hierarchy For Use In An Isolated Execution Environment
6	09/668,408		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
7	09/672,602		Attestation Key Memory Device And Bus
8	10/683,542		Controlling Access To Multiple Memory Zones In An Isolated Execution Environment
9	09/751,586		Resetting A Processor In An Isolated Execution Environment

09/538,954

Applicants respectfully request that the Examiner consider the cited reference(s), initial each reference on the enclosed Information Disclosure Statement to indicate such consideration, and return a copy of the initialed Information Disclosure Statement to Applicants. If the Examiner has any questions, the Examiner is invited to contact the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 9/15/04

MParré
Michael R. Barré
Patent Attorney
Intel Americas, Inc.
Registration No. 44,023
(512) 732-3927

c/o Blakely, Sokoloff, Taylor &
Zafman, LLP
12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1026

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